

# Inspection Certificate



Certificate No **968/INS 659.01/23**

Client / Certificate Owner  
ARM Ltd.  
110 Fulbourn Road  
Cherry Hinton  
Cambridge CB1 9NJ  
United Kingdom

Product  
General purpose microprocessor design including safety features

Type designation  
ARM Cortex-R5 Processor Revision r1p3

Standards applied for inspection  
IEC 61508 Parts 1-7:2010 (in extracts)  
ISO 26262 Parts 1-10:2011 (in extracts)

Inspection Results  
The assessment, based on the inspection programs INS FSP1 V1.0:2017 and INS FSP2 V1.0:2017 of the inspection body, concludes that the ARM Cortex-R5 Processor IP Core Revision r1p3 still complies with the requirements of IEC 61508:2010 for SIL 3 regarding the avoidance of systematic faults for a Compliant Item and still complies with the requirements of ISO 26262:2011 for ASIL D regarding the avoidance of systematic faults for a Safety-Element-out-of-Context (SEooC).  
As a result, the Cortex-R5 Processor can be used in safety-related applications up to SIL 3 according IEC 61508:2010 and up to ASIL D according to ISO 26262:2011.  
The requirements and constraints specified in the Cortex-R5 Safety Manual must be observed by the user.

Note: The expired TÜV Rheinland Industrie Service GmbH Certificate No. 968/FSP 1503.00/18 of the Certification Body Safety & Security for Automation & Grid is intended to be renewed after successful assessment of the ARM Cortex-R5 Processor IP Core Revision r1p3 with regard to the new requirements of ISO 26262:2018.

Note: This inspection certificate replaces 968/INS 659.00/23.

Inspection Period  
2023-01-02 - 2023-02-23

**TÜV Rheinland Industrie Service GmbH**  
Bereich Automation  
Funktionale Sicherheit  
Am Grauen Stein, 51105 Köln

Cologne, 2023-03-01

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Inspection Body of TÜV Rheinland Industrie Service GmbH

Automation - Functional Safety, [www.tuvasi.com](http://www.tuvasi.com)