

Safety related modules / components

Type Designation	Description	HW Rev.	SW Rev.	Firmware Rev.	Item Rev.	Report-No.:	Certification Status
PM01	Processor Module	A	<u>CPU:</u> RTSCORE0.bin CRC32: 475E36B1 RTSCORE0.binCRC CRC32: E11F4CAD RTSCORE1.bin CRC32: 88BB0DBB RTSCORE1.binCRC CRC32: 10CBFAD0 <u>u-boot-pm.bin</u> CRC32: B369FB35 <u>Power MCU:</u> PM01_APP_U40_01.hex CRC32: DB928166 PM01_L1BOOT_U40_01.hex CRC32: 9FCDB3B4 PM01_L2BOOT1_U40_01.hex CRC32: 6249E22E PM01_L2BOOT2_U40_01.hex CRC32: 1B47326B	<u>FPGA:</u> PM01_FPGA_U5_01.rbf CRC32: 3FFECE3B	PM01-A-V001	968/FSP 1454.00/17	<i>Valid</i>

Revision List
referred to on Certificate No.: 968/FSP 1454.00/17
Product: TSxPlus V1.0

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AI3281	Analog Input Module with HART Function	A	<u>MCU:</u> AI3281_APP_U224_U227_U230_01.hex CRC32: 9F29E04A AI3281_L1BOOT_U224_U227_U230_01.hex CRC32: 8F22B5E4 AI3281_L2BOOT1_U224_U227_U230_01.hex CRC32: 297C08F4 AI3281_L2BOOT2_U224_U227_U230_01.hex CRC32: F6A9A93B <u>Power MCU:</u> AI3281_APP_U312_01.hex CRC32: 1E5518B4 AI3281_L1BOOT_U312_01.hex CRC32: C0516A09 AI3281_L2BOOT1_U312_01.hex CRC32: 02DE6F82 AI3281_L2BOOT2_U312_01.hex CRC32: DE31DFDA	<u>FPGA:</u> AI3281_FPGA_U225_U228_U231_01.hex CRC32: 065EDC9C <u>CPLD:</u> AI3281_CPLD_U411_01.pof CRC32: 19F684A4	AI3281-A-V001	968/FSP 1454.00/17	<i>Valid</i>

Revision List
referred to on Certificate No.: 968/FSP 1454.00/17
Product: TSxPlus V1.0

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DI3201	Digital Input Module	A	<u>MCU:</u> DI3201_APP_U162_U166_U170_01.hex CRC32: D73D6BD5 DI3201_L1BOOT_U162_U166_U170_01.hex CRC32: 8F22B5E4 DI3201_L2BOOT1_U162_U166_U170_01.hex CRC32: 297C08F4 DI3201_L2BOOT2_U162_U166_U170_01.hex CRC32: F6A9A93B <u>Power MCU:</u> DI3201_APP_U235_01.hex CRC32: 1D452FBF DI3201_L1BOOT_U235_01.hex CRC32: 620CF64A DI3201_L2BOOT1_U235_01.hex CRC32: D77FDCF6 DI3201_L2BOOT2_U235_01.hex CRC32: 7EBCE6ED	<u>FPGA:</u> DI3201_FPGA_U164_U168_U172_01.hex CRC32: F30F6885 <u>CPLD:</u> DI3201_CPLD_U276_01.pof CRC32: 1C5B0247	DI3201-A-V001	968/FSP 1454.00/17	Valid

Revision List
referred to on Certificate No.: 968/FSP 1454.00/17
Product: TSxPlus V1.0

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AO1681	Analog Output Module with HART Function	A	<u>MCU:</u> AO1681_APP_U177_U180_U183_01.hex CRC32: 003BCED6 AO1681_L1BOOT_U177_U180_U183_01.hex CRC32: 8F22B5E4 AO1681_L2BOOT1_U177_U180_U183_01.hex CRC32: 297C08F4 AO1681_L2BOOT2_U177_U180_U183_01.hex CRC32: F6A9A93B <u>Power MCU:</u> AO1681_APP_U11_01.hex CRC32: 50958B5B AO1681_L1BOOT_U11_01.hex CRC32: 620CF64A AO1681_L2BOOT1_U11_01.hex CRC32: D77FDCF6 AO1681_L2BOOT2_U11_01.hex CRC32: 7EBCE6ED	<u>FPGA:</u> AO1681_FPGA_U178_U181_U184_01.hex CRC32: FC0ECDCC <u>CPLD:</u> AO1681_CPLD_U109_01.pof CRC32: A915A69D	AO1681-A-V001	968/FSP 1454.00/17	Valid

Revision List
referred to on Certificate No.: 968/FSP 1454.00/17
Product: TSxPlus V1.0

Type Designation	Description	HW Rev.	SW Rev.	Firmware Rev.	Item Rev.	Report-No.:	Certification Status
DO3201	Digital Output Module	A	<u>MCU:</u> DO3201_APP_U170_U174_U178_01.hex CRC32: 4238D633 DO3201_L1BOOT_U170_U174_U178_01.hex CRC32: 8F22B5E4 DO3201_L2BOOT1_U170_U174_U178_01.hex CRC32: 297C08F4 DO3201_L2BOOT2_U170_U174_U178_01.hex CRC32: F6A9A93B <u>Power MCU:</u> DO3201_APP_U246_01.HEX CRC32: 8BD1C591 DO3201_L1BOOT_U246_01.HEX CRC32: 620CF64A DO3201_L2BOOT1_U246_01.HEX CRC32: D77FDCF6 DO3201_L2BOOT2_U246_01.HEX CRC32: 7EBCE6ED	<u>FPGA:</u> DO3201_FPGA_U173_U177_U181_01.hex CRC32: BA210BEC <u>CPLD:</u> DO3201_CPLD_U309_01.pof CRC32: 719EA442	DO3201-A-V001	968/FSP 1454.00/17	Valid

Revision List
referred to on Certificate No.: 968/FSP 1454.00/17
Product: TSxPlus V1.0

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OSP01	Pulse Input Module with Over Speed Protection Function	A	<u>MCU:</u> OSP01_APP_U145_U149_U153_01.hex CRC32: 2C59FC86 OSP01_L1BOOT_U145_U149_U153_01.hex CRC32: 8F22B5E4 OSP01_L2BOOT1_U145_U149_U153_01.hex CRC32: 297C08F4 OSP01_L2BOOT2_U145_U149_U153_01.hex CRC32: F6A9A93B <u>Power MCU:</u> OSP01_APP_U239_01.hex CRC32: E841A20A OSP01_L1BOOT_U239_01.hex CRC32: 620CF64A OSP01_L2BOOT1_U239_01.hex CRC32: D77FDCF6 OSP01_L2BOOT2_U239_01.hex CRC32: 7EBCE6ED	<u>FPGA:</u> OSP01_FPGA_U148_U152_U156_01.hex CRC32: 55662691 <u>CPLD:</u> OSP01_CPLD_U334_01.pof CRC32: 86685F41	OSP01-A-V001	968/FSP 1454.00/17	Valid ¹

¹ OSP01 is in addition approved according to EN 62061:2005 + A1:2013 + A2:2015
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Revision List
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Product: TSxPlus V1.0

Type Designation	Description	HW Rev.	SW Rev.	Firmware Rev.	Item Rev.	Report-No.:	Certification Status
MC01	Main Chassis	A	-/-	-/-	MC01-A	968/FSP 1454.00/17	<i>Valid</i>
EC01	Expansion Chassis	A	-/-	-/-	EC01-A	968/FSP 1454.00/17	<i>Valid</i>
T-AI1601	Analog Input Terminal Module	A	-/-	-/-	T-AI1601-A	968/FSP 1454.00/17	<i>Valid</i>
T-DI1601	Digital Input Terminal Module	A	-/-	-/-	T-DI1601-A	968/FSP 1454.00/17	<i>Valid</i>
T-AO1601	Analog Output Terminal Module	A	-/-	-/-	T-AO1601-A	968/FSP 1454.00/17	<i>Valid</i>
T-DO1601	Digital Output Terminal Module	A	-/-	-/-	T-DO1601-A	968/FSP 1454.00/17	<i>Valid</i>
T-OSP01	Digital Input and Output Terminal Module	A	-/-	-/-	T-OSP01-A	968/FSP 1454.00/17	<i>Valid</i>
T-OSP02	Pulse Input Terminal Module	A	-/-	-/-	T-OSP02-A	968/FSP 1454.00/17	<i>Valid</i>

Non-safety related (non-interfering) modules / components

Type Designation	Description	HW Rev.	SW Rev.	Firmware Rev.	Item Rev.	Report-No.:	Certification Status
CM01	Communication Module	A	<u>CPU:</u> RTS1 CRC32: 4CD4669F RTS1CRC CRC32: 99C6FE30 cag.jffs2 CRC32: 5551E039 p1020rdb-pd.dtb CRC32: 4B91046A u-boot-cm.bin CRC32: CCFB4AE0 ulmage CRC32: 19D848C5 <u>Power MCU:</u> CM01_APP_U40_01.hex CRC32: C999481A CM01_L1BOOT_U40_01.hex CRC32: 9FCDB3B4 CM01_L2BOOT1_U40_01.hex CRC32: 6249E22E	<u>FPGA:</u> CM01_FPGA_U5_01.rbf CRC32: EE4AEB6F	CM01-A-V001	968/FSP 1454.00/17	Valid

Revision List
referred to on Certificate No.: 968/FSP 1454.00/17
Product: TSxPlus V1.0

Type Designation	Description	HW Rev.	SW Rev.	Firmware Rev.	Item Rev.	Report-No.:	Certification Status
			CM01_L2BOOT2_U40_01.hex CRC32: 1B47326B				
BI01	Bus Interface Module	A	-/-	<u>FPGA:</u> BI01_U34_01.jic CRC32: 942765B7	BI01-A-V001	968/FSP 1454.00/17	<i>Valid</i>
PW01	Power Supply	-/-	-/-	-/-	PW01-A-V001	968/FSP 1454.00/17	<i>Valid</i>

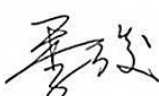


Software tools

Type Designation	Description	HW Rev.	SW Rev.	Firmware Rev.	Item Rev.	Report-No.:	Certification Status
ARCHITECT Suite – ARCHITECT	Configuration Software	-/-	-/-	-/-	V1.0.0	968/FSP 1454.00/17	<i>Valid</i>
ARCHITECT Suite – ARCHITECT MONITOR	Diagnostic Software	-/-	-/-	-/-	V1.0.0	968/FSP 1454.00/17	<i>Valid</i>
ARCHITECT Suite – ARCHITECT EVENT	SOE Software	-/-	-/-	-/-	V1.0.0	968/FSP 1454.00/17	<i>Valid</i>
ARCHITECT Suite – ARCHITECT SERVER	OPC Server	-/-	-/-	-/-	V1.0.0	968/FSP 1454.00/17	<i>Valid</i>

Safety Manual / User documentation

Document No.	Description	Report-No.:	Certification Status
[U1]	Safety Manual of TSxPlus V1.0.pdf	968/FSP 1454.00/17	Valid
[U2]	Planning and Installation Guide of TSxPlus V1.0.pdf	968/FSP 1454.00/17	Valid
[U3]	ARCHITECT User Manual V1.0.pdf	968/FSP 1454.00/17	Valid
[U4]	ARCHITECT EVENT User Manual V1.0.pdf	968/FSP 1454.00/17	Valid
[U5]	ARCHITECT MONITOR User Manual V1.0.pdf	968/FSP 1454.00/17	Valid
[U6]	ARCHITECT SERVER User Manual V1.0.pdf	968/FSP 1454.00/17	Valid

Signature

	Released by Manufacturer:	Released by Test Institute:	Released by Certifier:
Signatures:	 Jun Ji	 Yunxi Zhang	 Heinz Gall
Date:	2017-06-30	2017-06-30	2017-06-30

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Revision:

Date	Rev.	Description / Changes	Author
2017-06-30	1.0	Initial creation, based on Report-No.: 968/FSP 1454.00/17	yz/A-FS